

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,900,102 B2
APPLICATION NO. : 10/773022
DATED : May 31, 2005
INVENTOR(S) : Lee et al.

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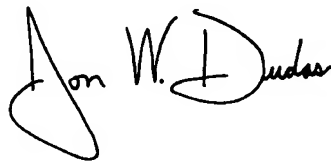
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 10.

Line 22 should read -- gate pattern includes $\text{Si}_{1-x}\text{Ge}_x$ wherein $0 < X \leq 1$. --

Signed and Sealed this

Fifth Day of February, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office